



09/964540

c/c

Docket No. : SON-2230/PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No. 6,889,164

Confirmation No. 2656

Inventor: Yukio OKUDA

Patented: May 3, 2006

For: METHOD AND APPARATUS OF  
DETERMINING DEFECT-FREE  
SEMICONDUCTOR INTEGRATED  
CIRCUIT

**REQUEST FOR CERTIFICATE OF CORRECTION**

MS: CERTIFICATE OF CORRECTION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**

NOV 13 2006

**of Correction**

Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the errors noted are errors of consequence involving claim language and thus warrant the granting of a Certificate of Correction.

Should any costs be incurred, please consider this authorization to charge Deposit Account No. 18-0013.

Dated: November 8, 2006

RADER, FISHMAN & GRAUER PLLC  
Suite 501, Lion Building  
1233 20<sup>th</sup> Street, N.W.  
Washington, D.C. 20036  
(202) 955-3750

Respectfully submitted,

Ronald P. Kananen  
Reg. No. 24,104

NOV 13 2006



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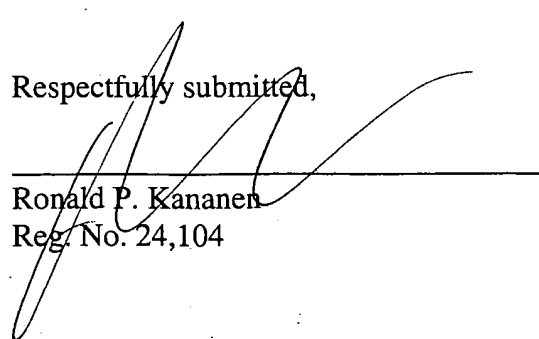
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PATENT NO. : 6,889,164

DATED : May 3, 2005

INVENTOR(S) : Yukio OKUDA

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 28:

Line 49, "deviation)T;" should read -- deviation); --.

MAILING ADDRESS OF SENDER:

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Washington, DC 20036

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**MAY 13 2006**